

AMENDMENTS TO THE SPECIFICATION

Please replace the abstract with the following amended paragraph marked up to show changes made relative to the immediate prior version:

According to some embodiments, an instruction is pre-decoded at a direct memory access unit. An instruction may be retrieved from a memory unit and partially pre-decoded at a direct memory access unit. The pre-decoded instruction may be provided from the direct memory access unit to a processing element, which in turn can completely decode and execute the instruction.